Rad Hard Non Volatile Memory for FPGA BootLoading [1]

Submitted by drupal on Wed, 10/23/2013 - 18:04

Firm: Space Micro, Inc. [2]

Award Solicitation: NASA SBIR 2012 Phase I Solicitation [3]

Award ID: SBIR_12_P1_124140

Award Topic: Radiation Hardened/Tolerant and Low Temperature Electronics and Processors [4]

Award Dollars: 124 590.00

Award Lead Center: Marshall Space Flight Center [5]

Proposal Number: H6.02-9860

Proposal Title: Rad Hard Non Volatile Memory for FPGA BootLoading


Firm PI First Name: Bert

Firm PI Last Name: Vermeire

Firm PI Phone: 8583320700

Firm PI Email: bvermeire@spacemicro.com

Firm Official First Name: David

Firm Official Last Name: Strobel

Firm Official Phone: 8583320700

Firm Official Email: dstrobel@spacemicro.com

Firm Zip: 92121

Firm Zip4: 1526

Firm State: California [8]

Firm City: San Diego

RI Street:
Rad Hard Non Volatile Memory for FPGA BootLoading

Space Micro, Inc.
RI City:

RI Zip: 00000
RI Zip4: 0000
Award Tech Area: Flight Computing [9]
RI Official First Name:

RI Official Last Name:
RI Official Phone:
RI Official Email:

Firm Street: 10237 Flanders Court
Migration ID: 2 736
Migration Firm ID: 988
Migration Solicit ID: 2
Award Tech Taxonomy: Circuits (including ICs; for specific applications, see e.g., Communications, Networking & Signal Transport; Control & Monitoring, Sensors) [10]