Rad Hard Non Volatile Memory for FPGA BootLoading

Submitted by drupal on Wed, 10/23/2013 - 18:04

Firm: Space Micro, Inc.

Award Solicitation: NASA SBIR 2012 Phase I Solicitation

Award ID: SBIR_12_P1_124140

Award Topic: Radiation Hardened/Tolerant and Low Temperature Electronics and Processors

Award Dollars: 124,590.00

Award Lead Center: Marshall Space Flight Center

Proposal Number: H6.02-9860

Proposal Title: Rad Hard Non Volatile Memory for FPGA BootLoading

Mission Directorate: Human Exploration and Operations

Selection: NASA 2012 SBIR Program Phase I Selections

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RI Street:

Firm Name:
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RI Zip4:
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Award Tech Area:
Flight Computing [9]

RI Official First Name:

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Migration ID:
2 736
Migration Firm ID:
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Migration Solicit ID:
2

Award Tech Taxonomy:
Circuits (including ICs; for specific applications, see e.g., Communications, Networking & Signal Transport; Control & Monitoring, Sensors) [10]